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Title:A PLL And Method For Providing A Single/Multiple Adjustable Frequency Range Applicants: Roxanne Vu, et al. Docket: RAMB-01052US0

Applicants: Roxanne Vu, et al.
Appl. No.: Unknown
Filing Date: April 21, 2004
Express Mail No.: EV 391867051 US

Atty: Phone: Kirk J. DeNiro, Esq. (415) 369-9660

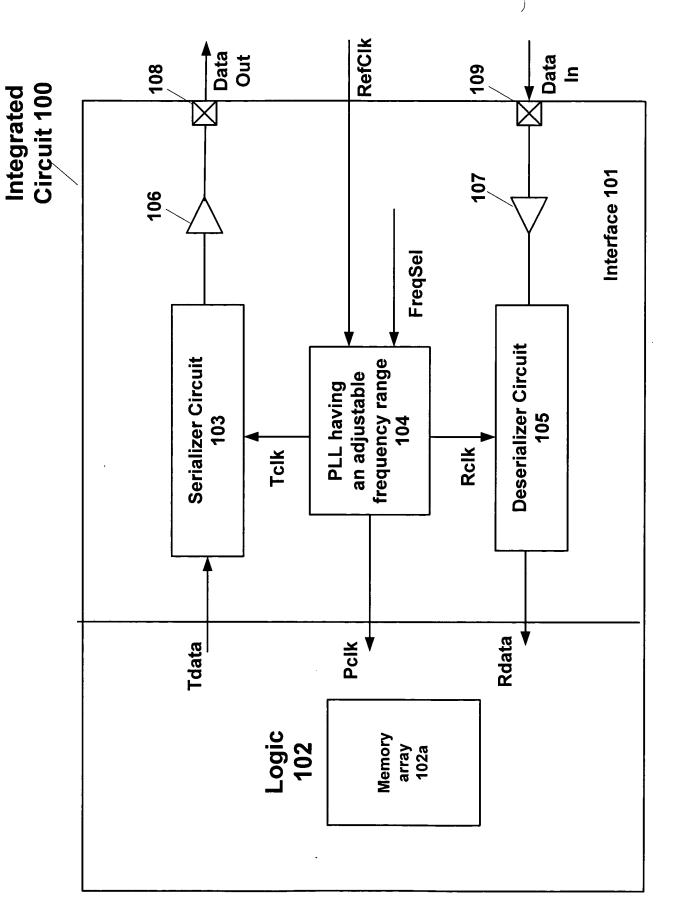
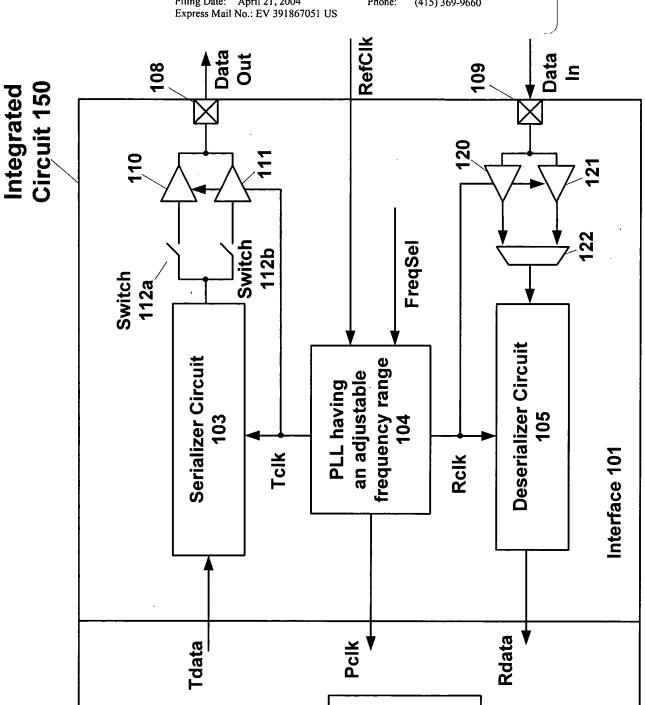


Fig. 1a

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Logic 102

Memory

array 102a

Fig. 1b

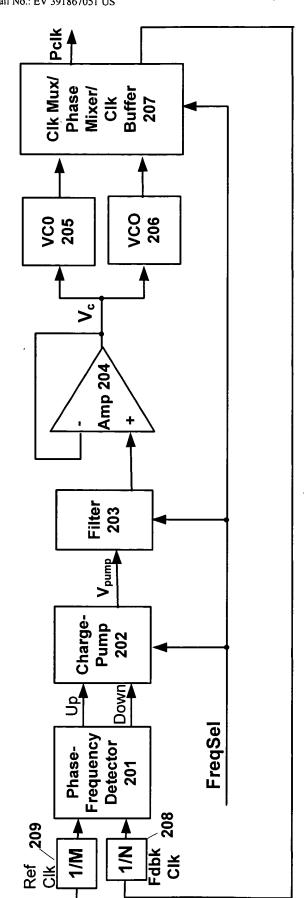
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PLL 200

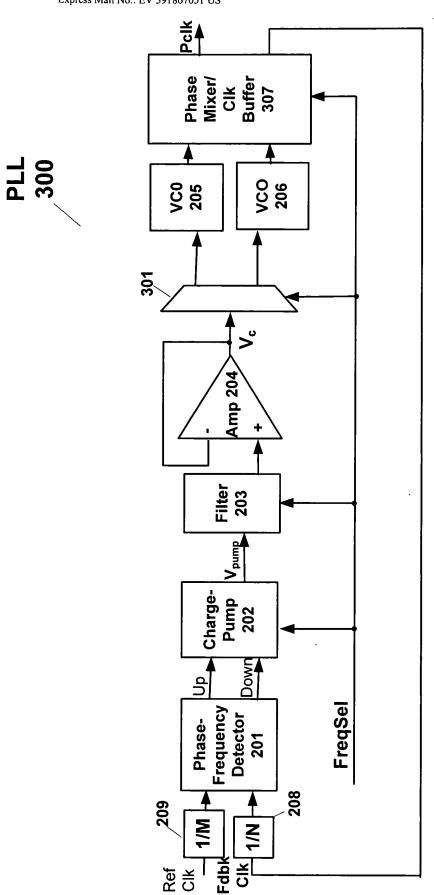
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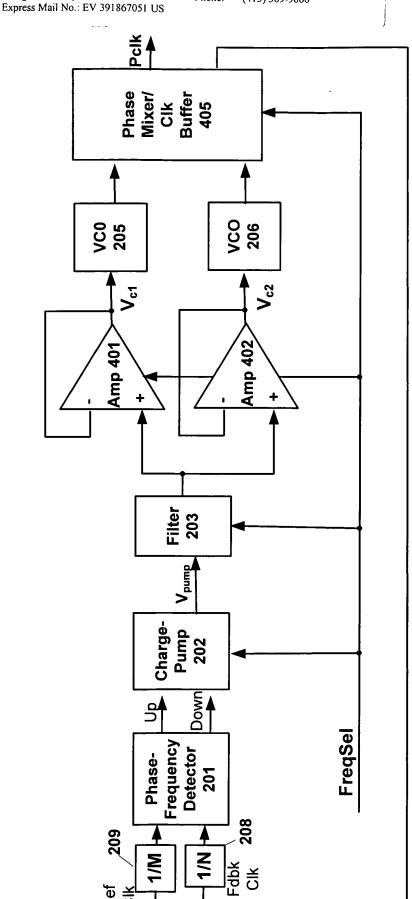
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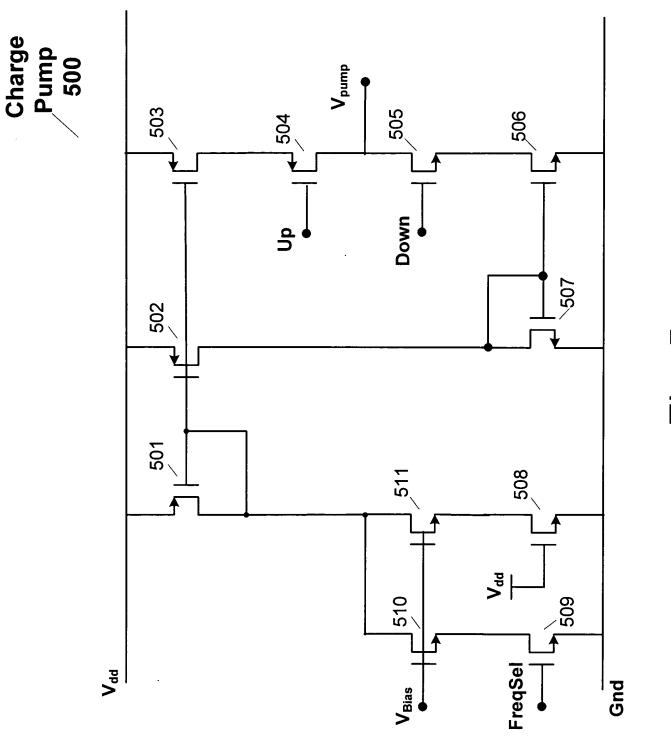


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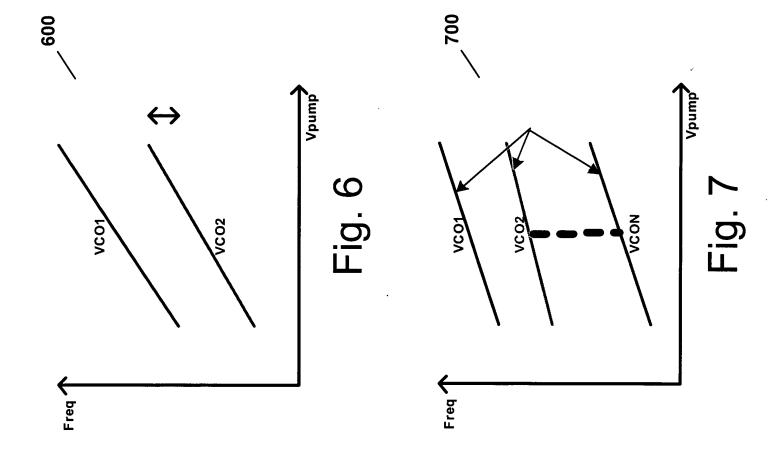
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